

Atty. Docket No.

Serial No.

INFORMATION DISCLOSURE  
CITATION

(Use several sheets if necessary)

723-840

09/545,182

Applicant

LIAO et al.

Filing Date

Group

April 7, 2000

2782



## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
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K	4,841,438	06/1989	Yoshida et al.			
W	4,881,168	11/1989	Inagami et al.			
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\*Examiner

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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

## **INFORMATION DISCLOSURE CITATION**

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TRADEMARKS	5,931,945	08/1999	Yung et al.	AUG 27 2003
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## **FOREIGN PATENT DOCUMENTS**

## **TRANSLATION**

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	IBM, "PowerPC 740 and PowerPC 750 RISC Microprocessor Family User Manual", 1998
	MIPS Technologies, Inc., "Silicon Graphics Introduces Enhanced MIPS® Architecture to Lead the Interactive Digital Revolution – Future Digital Media Processors Will Enable New World of High-Performance and Low-Cost Interactive Digital Applications, 1996
	Web Extension I: Survey of RISC Architectures
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	IEEE, "Subword Parallelism With MAX-2", 1996

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